



B.E/B.Tech., DEGREE EXAMINATION APRIL/MAY—2015

THIRD SEMESTER

ELECTRONICS AND COMMUNICATION ENGINEERING

EC2205/ELECTRONIC CIRCUITS—I

(Regulation 2008)

PART- A

1. What are the factors to be considered to design a biasing circuit?
2. Define operating point.
3. Draw the h-parameter equivalent circuit for common emitter amplifier.
4. State Miller's theorem.
5. Discuss the effect of bypass capacitor on bandwidth of the amplifier.
6. If the mid band gain of an amplifier is 100 and the half power frequencies are $f_L=40\text{Hz}$ and $f_H=16\text{ kHz}$. Calculate the amplifier gain at 20 Hz and 20 kHz frequency.
7. Define conversion efficiency of power amplifier.
8. What is cross over distortion in class B power amplifier?
9. Define PIV.
10. What are the advantages of SMPS over linear regulators?

PART-B

- 11.(a) Define stability factor. Calculate the operating point for the voltage divider biasing circuit shown in fig 1. Assume that the transistor is a silicon transistor with $\beta_{dc}=100$, $R_1=9.1\text{Kohm}$, $R_2=4.7\text{Kohm}$, $R_c = 1\text{kohm}$, $R_E=560\text{ohm}$, $V_{cc}=19\text{ V}$. Find its stability factor.

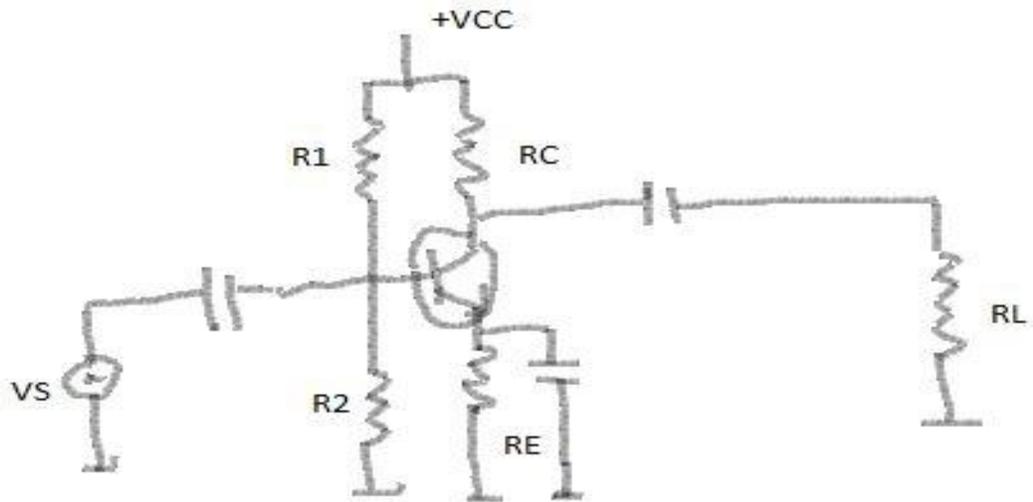


Fig. 1

Or

- b) What is bias compensation? Explain diode compensation
i) for V_{be} ii) for I_{co}

12.(a) for the amplifier shown in fig 2, calculate:

- i) The overall current gain (A_{Is}).
- ii) Overall voltage gain (A_{vs}).
- iii) Overall input resistance R_i' .
- iv) Overall output resistance R_o' .

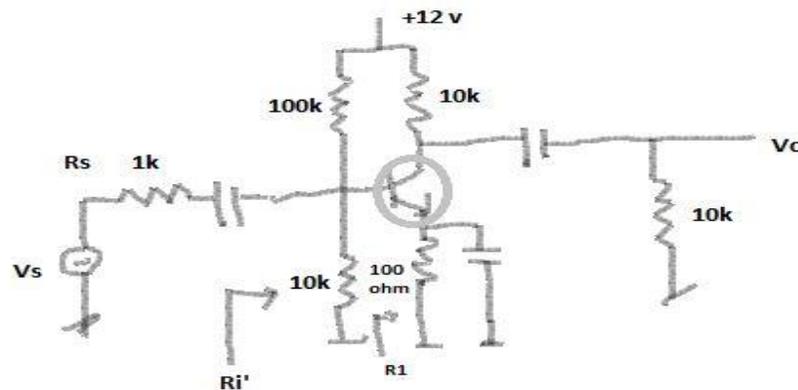


Fig. 2



Or

b) explain the operation of emitter coupled difference amplifier with constant current load to improve stability. Briefly explain the transfer characteristics of a differential amplifier.

13.(a) show that the maximum efficiency of transistor coupled amplifier is 50% discuss the advantages and disadvantages of transformer coupled class A amplifier.

Or

b) with a neat circuit diagram explain the working of class B push pull power amplifier. What are its advantages and disadvantages?

14.(a) i) Derive the relation between rise time and upper 3db frequency of an amplifier.

ii) four identical stages are cascaded. The lower and upper 3db frequencies of each stage are 40Hz and 20KHz respectively. Calculate the overall bandwidth of the cascaded amplifier.

Or

b) Derive expression for upper and lower cutoff frequencies of a capacitively coupled multistage amplifier.

15.(a) A half wave rectifier circuit is supplied from 230V, 50Hz supply with a stepdown ratio of 3:1 to resistive load of 10kohm. The diode forward resistance is 75ohm while transformer secondary resistance is 10ohm. Calculate:

- i) The maximum value of current.
- ii) Average value of current.
- iii) RMS value of current.
- iv) D.c output voltage.
- v) Rectification efficiency.
- vi) Ripple factor.



Or

b) with a neat circuit diagram. Explain the working of:

- i) buck switching regulator.
 - ii) boost switching regulator.
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